Sabine Broda

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12 de Novembro de 2014

Sabine Broda (DCC-FCUP)

Formal Verification of Software

12 de Novembro de 2014 1 / 26

- Area of computer science that studies and applies mathematical methods for proving the correctness of software systems/programs with respect to a formal specification or property.
- The dissemination and importance of the role of information systems in essential sectors of society is continually increasing, demanding more and more for the certification/guarantee of their reliability.
- This is particularly crucial when *critical systems* are concerned, such as traffic control, nuclear or medical equipment.
- Some facts:
 - the cost of maintaining software is about 66% of its total cost;
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- Illustration of two standard approaches to formal verification by example:
 - Model checking;
 - Deductive program verification.
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 - KAT (Kleene Algebra with Tests);
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Model Checking

- technique for the verification of (finite-state) reactive/concurrent systems;
- systems are represented by a transition system (the model);
- specifications/properties are expressed by formulae of temporal logic (LTL/CTL);
- a model checker (efficient symbolic algorithm) decides if the specification is true in the model;
- if a property is not valid, a counterexample is exhibited;
- in general this method is automatic for finite models;
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- Deductive system that can be used to assert the correctness of a program with respect to a given specification by constructing a derivation.
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When concurrent processes share a resource (such as a file on a disk or a database entry), it may be necessary to ensure that they do not have access to it at the same time.

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- n_i be in it's non-critical state
- t_i trying to enter it's critical state, or
- c_i be in it's critical state

Each individual process undergoes transitions in the cycle $n_i \rightarrow t_i \rightarrow c_i \rightarrow n_i \rightarrow \cdots$, but the two processes interleave with each other.

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A first-attempt model for mutual exclusion



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Safety Only one process is in its critical section at any time. φ : $G \neg (c_1 \land c_2)$

Liveness Whenever any process requests to enter its critical section, it will eventually be permitted to do so. $\psi : G(t_1 \rightarrow Fc_1) \land G(t_2 \rightarrow Fc_2)$

Here *G* and *F* are temporal conectives of LTL (Linear Temporal Logic) such that,

- M, s₀ ⊨ Gθ iff for every execution path starting in s₀ the formula θ is globally true (i.e. in all states);
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A second model satisfying both properties

In order for *liveness* to be true it is sufficient to divide the state labelled with t_1t_2 into two different states:



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- $\mathcal{M}, s \models AG \theta$ iff for All computation paths beginning in s the property θ holds Globally (i.e. in all states);
- M, s ⊨ EF θ iff there Exists a computation paths beginning in s such that θ holds in some Future state.

Note: this property cannot be expressed in LTL!

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SMV code for mutual exclusion

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```
MODULE main
VAR.
   pr1: process prc(pr2.st, turn, 0);
   pr2: process prc(pr1.st, turn, 1);
   turn: boolean;
ASSIGN
   init(turn) := 0;
-- safety
LTLSPEC G!((pr1.st = c) \& (pr2.st = c))
-- liveness
LTLSPEC G((pr1.st = t) \rightarrow F(pr1.st = c))
LTLSPEC G((pr2.st = t) \rightarrow F(pr2.st = c))
```

SMV code for mutual exclusion

```
MODULE prc(other-st, turn, myturn)
VAR.
   st: {n, t, c};
ASSIGN
   init(st) := n;
   next(st) :=
       case
          (st = n)
                                                           : {t,n};
          (st = t) \& (other-st = n)
                                                           : c:
          (st = t) \& (other-st = t) \& (turn = myturn): c;
          (st = c)
                                                           : {c,n};
          1
                                                           : st;
       esac;
   next(turn) :=
       case
          turn = myturn & st = c : !turn;
          1
                                    : turn;
       esac;
FAIRNESS running
FAIRNESS !(st = c)
                                                               (4) E (4) E (4)
                                                                          Sabine Broda (DCC-FCUP)
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- But what kind of algorithms and mathematical structures are at the core of these tools?

Problem: Given an LTL-formula ϕ , a model \mathcal{M} and a state s, check if $\mathcal{M}, s \models \phi$.

Approach:

- Construct an automata $\mathcal{A}_{\neg\phi}$ that accepts a computation path π iff $\pi \models \neg \phi$, i.e. $\pi \not\models \phi$.
- Represent (\mathcal{M}, s) by an automata $\mathcal{A}_{\mathcal{M},s}$ (that accepts exactly the computation paths in \mathcal{M} that start in s).
- Check if L(A_{¬φ}) ∩ L(A_{M,s}) = Ø. In this case one has M, s ⊨ φ, otherwise, every path belonging to the intersection of the two languages can be exhibited as a counter-example.

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- Represent (\mathcal{M}, s) by an automata $\mathcal{A}_{\mathcal{M},s}$ (that accepts exactly the computation paths in \mathcal{M} that start in s).
- Check if $\mathcal{L}(\mathcal{A}_{\neg \phi}) \cap \mathcal{L}(\mathcal{A}_{\mathcal{M},s}) = \emptyset$. In this case one has $\mathcal{M}, s \models \phi$, otherwise, every path belonging to the intersection of the two languages can be exhibited as a counter-example.

- Computation paths are represented by infinite sequences of states. Ex.: $\{n_1, n_2\}\{n_1, t_2\}\{t_1, t_2\}\{t_1, c_2\}\{t_1, n_2\}\dots$
- Büchi automata are a type of automata that extends finite automata to process (accept/reject) infinite words (different acceptance criteria).
- In an alternating automaton the transition function is a partial function δ : S × Σ → B⁺(S), where B⁺(S) is the set of boolean formulas built from elements in S and conectives ∨ (representing existential choice) and ∧ (representing universal choice). Ex.: δ(s, a) = (s₁ ∧ s₂) ∨ (s₃ ∧ s₄).
- Alternating Büchi automata have the same expressive power as nondeterministic Büchi automata, but are much more succint (alternating Büchi automaton → exponential blow-up → nondeterministic Büchi automaton).

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Model checking algorithms with OBDD's

- An OBDD (Ordered Binary Decision Diagram) is a data structure used to represent boolean functions, providing compact representations for sets or relations.
- Given a model \mathcal{M} , sets of states of \mathcal{M} , as well as the transition relation of \mathcal{M} can be encoded by OBDD's.
- An algorithm for deciding CTL-logic (the labelling algorithm) can operate directly on the encodings (OBDD's).
- SMV programs can be compiled directly into OBDD's without having to go via intermediate representations (bigger size).

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- It builds on first-order logic, dealing with the notion of correctness of a program w.r.t. a given specification.
- The specification consists of a *precondition* and a *postcondition*.
- Correctness of a program is asserted by constructing a derivation in the inference system of Hoare logic.
- While doing so, onde must identify an *invariant* for every loop in the program.
- In the system presented here loop invariants are given beforehand by the programmer as an input to the program verification process (and not invented during the construction of the proof).
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$$\overline{\{\phi\}\operatorname{skip}\{\psi\}} \quad \text{if} \models \phi \to \psi$$

$$\overline{\{\phi\}\, x := E\,\{\psi\}} \quad \text{if } \models \phi \to \psi[E/x]$$

$$\frac{\{\phi\} C_1 \{\eta\} \quad \{\eta\} C_2 \{\psi\}}{\{\phi\} C_1; C_2 \{\psi\}}$$

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Sabine Broda (DCC-FCUP)

Formal Verification of Software

12 de Novembro de 2014 19 / 26

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- System \mathcal{H}_g contains an implicit strategy for constructing a proof/derivation of an assertion $\{\phi\}C\{\psi\}$ in a deterministic way.
- During the construction of a proof, side conditions (verification conditions) are created, that have to be checked to hold by some proof tool.
- For the sequence rule $\{\phi\}C_1$; $C_2\{\psi\}$ an intermediate formula η has to be guessed. For this, the weakest precondition η verifying $\{\eta\}C_2\{\psi\}$ is used.

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A VCGen algorithm: computation of the weakest preconditions of a program (wp)

Given a program C and a postcondition ϕ , one can compute $wp(C, \phi)$ by the following rules. Then, $\{wp(C, \phi)\}C\{\phi\}$ holds and, furthermore, if $\{\psi\}C\{\phi\}$ holds for some ψ then $\psi \to wp(C, \phi)$.

$$\begin{split} wp(\text{skip},\phi) &= \phi \\ wp(x := E, \phi) &= \phi[E/x] \\ wp(C_1; C_2, \phi) &= wp(c_1, wp(C_2, \phi)) \\ wp(\text{if } B \text{ then } C_1 \text{ else } C_2, \phi) &= (B \to wp(C_1, \phi) \\ & \wedge (\neg B \to wp(C_2, \phi)) \\ wp(\text{while } B \text{ do } \{\eta\} C, \phi) &= \eta \end{split}$$

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VCGen algorithm

First, function VC computes a set of verification conditions, without taking the precondition into acount:

Finally, the precondition has to imply the weakest precondition, which is required for ϕ to hold after the execution of C:

$VCG(\{\psi\}C\{\phi\}) = \{\psi \to wp(C,\phi)\} \cup VC(C,\phi)$

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General architecture of a program verification system



Exemplo

Consider the following annotated program fact:

```
f:=1; i:=1;
while i<= n do {f = fact(i-1) and i <= n+1} {
    f:=f*i;
    i:=i+1;
}
```

We will compute

$$VCG(\{n \ge 0\} fact\{f = n!\})$$

using the following abbriations:

 $\theta = f = (i - 1)! \land i \le n + 1$ and $C_w = f := f * i; i := i + 1.$

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using the following abbriations:

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 and $C_w=f:=f*i;i:=i+1.$

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$$VC(\texttt{fact}, f = n!)$$

$$= VC(f := 1; i := 1; wp(while i \le n \operatorname{do}\{\theta\}C_w, f = n!))$$

$$\cup VC(while i \le n \operatorname{do}\{\theta\}C_w, f = n!)$$

$$= VC(f := 1; i := 1, \theta) \cup \{\theta \land i \le n \to wp(C_w, \theta)\}$$
$$\cup \{\theta \land i > n \to f = n!\} \cup VC(C_w, \theta)$$

$$= VC(f := 1, wp(i := 1, \theta)) \cup VC(i := 1, \theta)$$

$$\cup \{f = (i - 1)! \land i \le n + 1 \land i \le n \to wp(f := f * i; i := i + 1, \theta)\}$$

$$\cup \{f = (i - 1)! \land i \le n + 1 \land i > n \to f = n!\}$$

$$\cup VC(f = f * i, wp(i := i + 1, \theta)) \cup VC(i := i + 1, \theta)$$

$$= \emptyset \cup \emptyset \cup \{f = (i-1)! \land i \le n+1 \land i \le n \\ \to wp(f := f * i, f = (i+1-1)! \land i+1 \le n+1)\} \\ \cup \{f = (i-1)! \land i \le n+1 \land i > n \to f = n!\} \cup \emptyset \cup \emptyset \\ = \{f = (i-1)! \land i \le n+1 \land i \le n \to f * i = (i+1-1)! \land i+1 \le n+1, \\ f = (i-1)! \land i \le n+1 \land i > n \to f = n!\}$$

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$$\begin{split} & VCG(\{n \geq 0\} \texttt{fact}\{f = n!\}) \\ &= \{n \geq 0 \to wp(\texttt{fact}, f = n!)\} \cup VC(\texttt{fact}, f = n!) \\ &= \{n \geq 0 \to wp(\texttt{f} := 1; i := 1; wp(\texttt{while } i \leq n \ \texttt{do}\{\theta\}C_w, f = n!)\} \\ &\{f = (i-1)! \land i \leq n+1 \land i \leq n \to f * i = (i+1-1)! \land i+1 \leq n+1, \\ &f = (i-1)! \land i \leq n+1 \land i \leq n \to f = n!\} \\ &= \{n \geq 0 \to wp(\texttt{f} := 1; i := 1; \theta)\} \\ &\{f = (i-1)! \land i \leq n+1 \land i > n \to f * i = (i+1-1)! \land i+1 \leq n+1, \\ &f = (i-1)! \land i \leq n+1 \land i > n \to f = n!\} \end{split}$$

The following proof obligations are generated:

$$n \ge 0 \to 1 = (1-1)! \land 1 \le n+1$$
 $f = (i-1)! \land i \le n+1 \land i \le n \to f * i = (i+1-1)! \land i+1 \le n+1$
 $f = (i-1)! \land i \le n+1 \land i > n \to f = n!$